# DATA SHEET

# mos field effect transistor $\mu$ **PA610TA**

## P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR HIGH SPEED SWITCHING

## DESCRIPTION

EC

The  $\mu$ PA610TA is a switching device which can be driven directly by a 2.5 V power source.

The  $\mu$ PA610TA has excellent switching characteristics, and is suitable for use as a high-speed switching device in digital circuits.

#### **FEATURES**

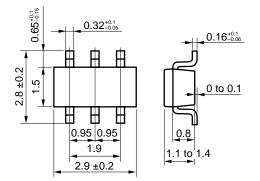
- Can be driven by a 2.5 V power source.
- Low Gate Cut-off Voltage.

### ABSOLUTE MAXIMUM RATINGS (T<sub>A</sub> = 25 $^{\circ}$ C)

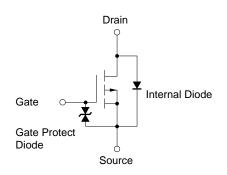
Drain to Source Voltage	Vdss	-30	V
Gate to Source Voltage	Vgss	∓20	V
Drain Current (DC)	D(DC)	∓0.1	А
Drain Current (pulse)	D(pulse)	∓0.4 Note	А
Total Power Dissipation	Р⊤	300 (TOTAL)	mW
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

**Note** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1 %

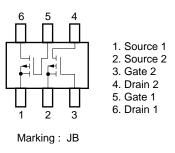
#### Package Drawings (unit: mm)



#### **Equivalent Circuit**



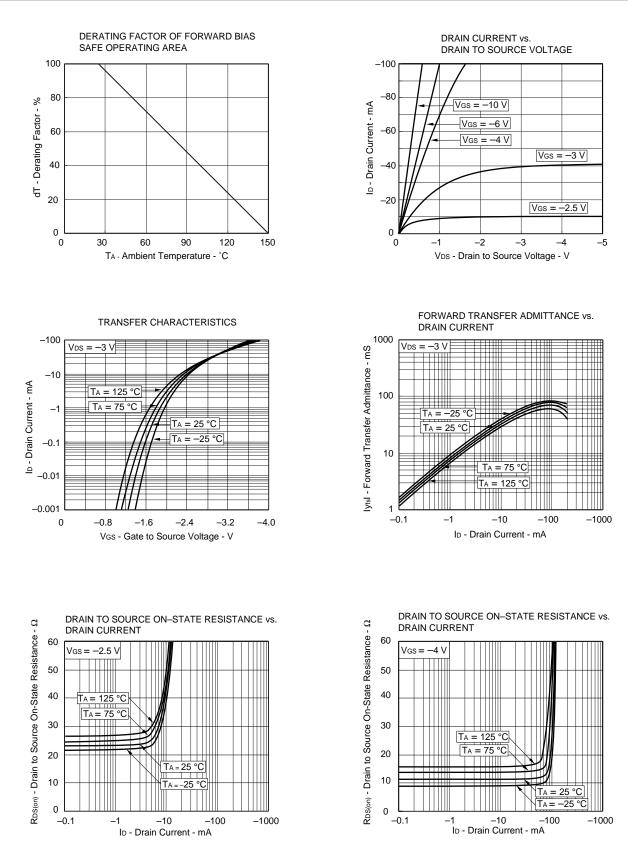
#### Pin Connection (Top View)

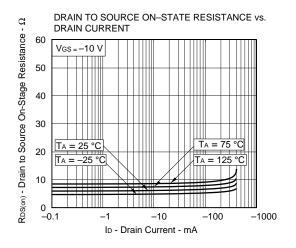


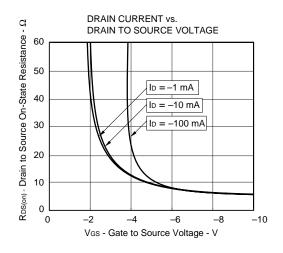
The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device is actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25 °C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Drain Cut-off Current	Ibss			-1	μΑ	$V_{DS} = -30 V, V_{GS} = 0$
Gate Leakage Current	lgss			∓10	μΑ	$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0$
Gate Cut-off Voltage	VGS(off)	-1.0	-1.4	-1.7	V	$V_{DS} = -3 V$ , $I_D = -10 \mu A$
Forward Transfer Admittance	y <sub>fs</sub>	20			mS	$V_{DS} = -3 V, I_{D} = -10 mA$
Drain to Source On-State Resistance	RDS(on)1		23	60	Ω	$V_{GS} = -2.5 \text{ V}, \text{ Id} = -1 \text{ mA}$
Drain to Source On-State Resistance	RDS(on)2		11	23	Ω	$V_{GS} = -4 V$ , $I_D = -10 mA$
Drain to Source On-State Resistance	RDS(on)3		6	13	Ω	$V_{GS} = -10 \text{ V}, \text{ Id} = -10 \text{ mA}$
Input Capacitance	Ciss		5		pF	$V_{DS} = -3 V$
Output Capacitance	Coss		15		pF	Vgs = 0
Reverse Transfer Capacitance	Crss		1.3		pF	f = 1 MHz
Turn-on Delay Time	td(on)		140		ns	$V_{DD} = -3 V, I_D = -10 mA$
Rise Time	tr		330		ns	$V_{GS(on)} = -4 V, R_G = 10 \Omega$
Turn-off Delay Time	td(off)		220		ns	RL = 300 Ω
Fall Time	tr		320		ns	

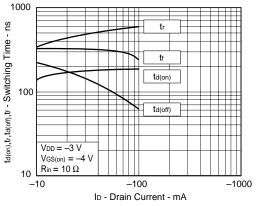


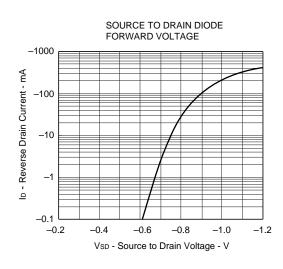




CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE 100 VGS = 0f = 1 MHzCiss, Coss, Crss - Capacitance - pF Coss 10 Ciss Crss 1 -10 -100 -1 VDS - Drain to Source Voltage - V







## REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system	TEI-1202
Quality grade on NEC semiconductor devices	C11531E
Semiconductor device mounting technology manual	C10535E
Guide to quality assurance for semiconductor devices	MEI-1202
Semiconductor selection guide	X10679E

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Anti-radioactive design is not implemented in this product.

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